

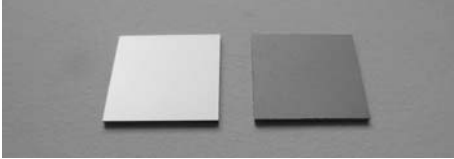


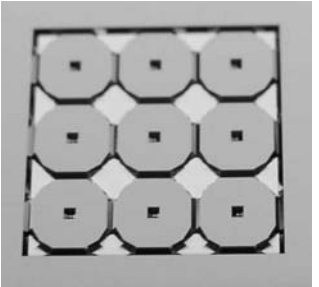



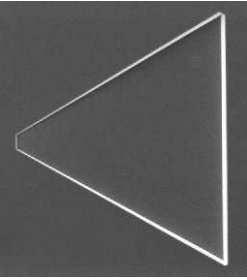
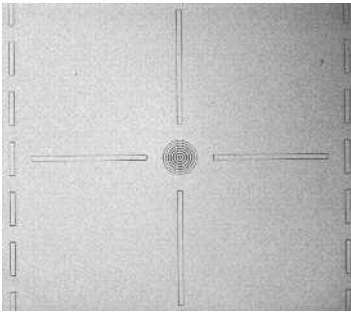
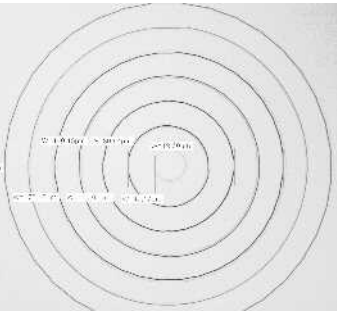
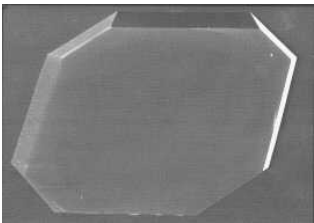
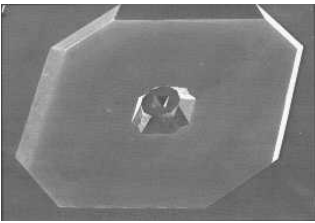
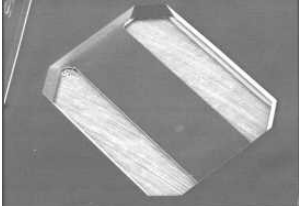
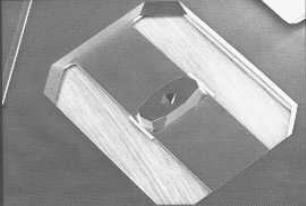
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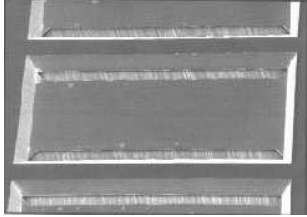
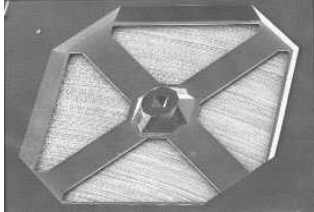

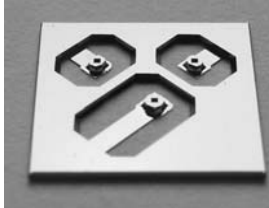


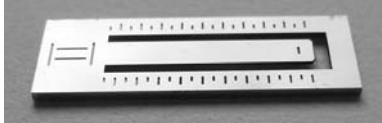

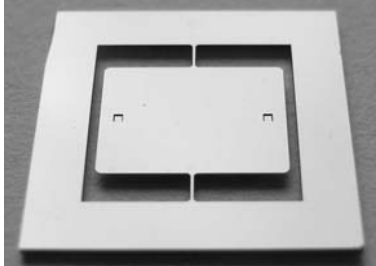

Treasure Chest

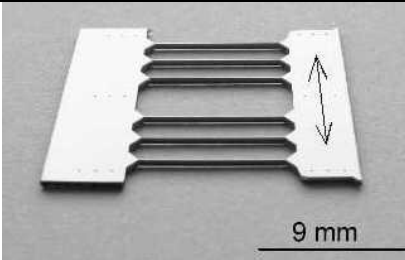
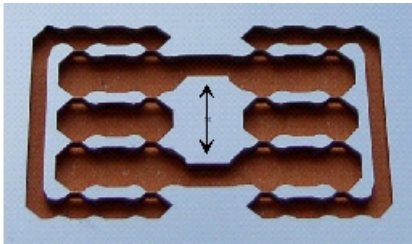
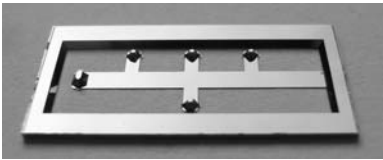
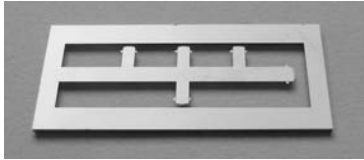
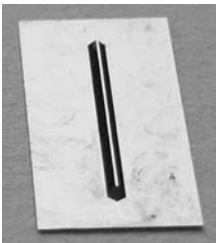

Examples of available chips and structures

Do not hesitate to tell us your requests

Chip resp. Structure (Measures rounded)	Type	View	
<p>{100}Si-Chip without Structure (further orientations, sizes, thicknesses and coatings by request)</p> <p>Square (sawed along <110>) square 10 mm x 10 mm, 525 µm thick, 2-side polished square 10 mm x 10 mm, 525 µm thick, front polished, back lapped</p> <p>Stripe (long side etched, narrow side sawed) rectangular 4 mm x 20 mm, 525 µm thick, 2-side polished, along <110> rectangular 4 mm x 20 mm, 525 µm thick, 2-side polished, along <100> rectangular 4 mm x 40 mm, 525 µm thick, 2-side polished, along <110> rectangular 4 mm x 40 mm, 525 µm thick, 2-side polished, along <100></p>	<p>CQ10-525pp CQ10-525pl</p> <p>C4:20P-525pp C4:20D-525pp C4:40P-525pp C4:40D-525pp</p>	 <p>Type CQ10-525pp (left) Type CQ10-525pl (right)</p>  <p>Type CR4:20P-525pp (top) Type CR4:40P-525pp (bottom)</p>	
<p>Concave (Hollow)-Pyramids inside an {100}Si-Chip square pyramid, length of edges 410 µm, depth 290 µm</p> <p>Single pyramid chip size 3 mm x 3 mm about dodecagonal, thickness 300 µm</p> <p>Array of 3 x 3 pyramids chip size 10 mm x 10 mm, thickness 300 µm</p>	<p>HP3</p> <p>HP3x3</p>	 <p>Type HP3</p>	 <p>Type HP3x3</p>

<p>Chip with Deepenings of Increasing Depth (3°-Ramp) {111}Si tilted about 3° around <110> trapezoidal deepening: basis 250 µm, height 150 µm, trapezoidal angle 60° depth increasing from the narrow side (1 µm) up to the basis (8 µm) chip size 10 mm x 10 mm</p> <p><u>5 Groups of ramps</u> (cross like arranged) each with 5 trapezes</p>	<p>TR3</p>	 <p>Group of trapezes</p>	 <p>Single-trapeze (ramp)</p>
<p>Chip with Systems of Rings dry etched, other depths by request system of rings: - circular deepening in the centre, diameter 20 µm - 3 rings (width 15 µm), outer diameter 80, 140, 200 µm chip with 3 x 3 systems of rings (distance 2 mm) inside a centre of crosses chip size 10 mm x 10 mm</p> <p><u>System with depths</u> 100, 200, 300, 400 nm from the centre up to the outside <u>System with depths</u> 400, 300, 200, 100 nm from the centre up to the outside <u>System with depths</u> 300, 100, 200, 400 nm from the centre up to the outside <u>System with depths</u> 200, 400, 100, 300 nm from the centre up to the outside</p>	<p>RS100:400 RS400:100 RS300:400 RS200:300</p>	 <p>Rings inside the cross</p>	 <p>System of rings</p>
<p>Chip with a Deepening (Membrane) depth 350 µm, thickness of the membrane 50 µm (others by request) membrane 6 mm x 6 mm (corners blunted) boss: stump of an 8-angular pyramid, top 1 mm x 1 mm chip size 10 mm x 10 mm</p> <p><u>Membrane without boss</u> <u>Membrane with boss in the centre</u></p>	<p>M10-50 MB10-50</p>	 <p>Type M 10-50</p>	 <p>Type MB 10-50</p>
<p>Chip with Structured Membrane depth 350 µm, thickness of the membrane 50 µm (others by request) boss: stump of a pyramid chip size 10 mm x 10 mm</p> <p><u>Stripe-membrane (bridge)</u> along <100>, width 2 mm, without boss along <100>, width 2 mm, with boss (2 mm x 1 mm)</p>	<p>BD10-50 BDB10-50</p>	 <p>Type BD10-50</p>	 <p>Type BDB10-50</p>

<p><u>3 Stripe-membranes (bridges)</u> along $\langle 110 \rangle$ without boss, width 1, 2, 1 mm along $\langle 110 \rangle$ with boss, width 1, 2, 1 mm</p> <p><u>Crosslike-membrane</u> along $\langle 100 \rangle$ with a central boss (1 mm x 1 mm), width 1 mm</p>	<p>3BP10-50 3BPB10-50</p> <p>KFDB10-50</p>	 <p>Type BP10-50</p>  <p>Type KFDB 10-50</p>	 <p>Type BPB10-50</p>
<p>Chip with Cantilever cantilever thickness 50 μm (others by request)</p> <p><u>3 Cantilevers along $\langle 100 \rangle$</u>, width 1 mm, length 1, 1, 3 mm, boss 1 mm x 1 mm <u>3 Cantilevers along $\langle 110 \rangle$</u>, width 1, 3, 1 mm, length 6 mm, boss 1(3)mm x 3 mm <u>4 Cantilevers along $\langle 110 \rangle$</u>, width 1 mm, length 1 mm, boss 2 mm x 1 mm chip size 10 mm x 10 mm</p> <p><u>1 Cantilever along $\langle 110 \rangle$</u>, width 1.5 mm, length 8 mm, with boss 1 x 1 mm chip size 5 mm x 15 mm frame with scale</p>	<p>3CD-50 3CP-50 4CP-50</p> <p>1CP-50</p>	 <p>Type 3CD-50</p>  <p>Type 3CP-50</p>  <p>Type 4CP-50</p>  <p>Type 1CP-50 front side</p>  <p>back side</p>	
<p>Chip with Torsional Plate (Mirror) plate 4 mm x 6 mm, thickness 50 μm frame for stiffening at the back side no metallization torsional beams at the broadside of the plate: thickness 50 μm, length 1 mm width 0.1 mm chip size 10 mm x 10 mm</p>	<p>TS10-50</p>	 <p>Type TS10-50 front side</p>	 <p>back side</p>

<p>Parallel springs (in plane-bending) size 20 mm x 16 mm springs: width 525 μm (thickness of the chip), length about 9 mm mounting bar 5 mm x 16 mm coupling bar 3 mm x 16 mm thickness of springs 50 μm</p> <p>2x1-Spring system 2x2-Spring system 2x3-Spring system</p>	<p>PSS1 PSS2 PSS3</p>	 <p>Type PSS3</p>	
<p>Straight guide (horizontal) (solid hinges: in plane-bending)</p> <p>stage about 4 mm x 4 mm, travel up to +/-1 mm, load max. 0.1 N chip size 30 mm x 40 mm, thickness 1.2 mm</p>	<p>SGH1</p>	 <p>Type SGH1</p>	
<p>Hall-Structure electrical resistivity 0.1 Ohmcm stripe: width 2 mm, thickness 50 μm bosses for electrical contacts (without metallisation) for current, potential and Hallvoltage chip size 5 mm x 20 mm</p>	<p>HE-50</p>	 <p>Type HE-50 front side</p>	 <p>back side</p>
<p>Chip with Edges height of edges 350 μm trapezoidal cross section (narrow side 10 μm) thickness of the membrane 50 μm (others by request)</p> <p>2 Edges, short (4 mm, distance 10 mm) chip size 5 mm x 15 mm 1 Edge, long (8 mm) chip size 5 mm x 10 mm</p>	<p>E10-350 2E10-350</p>	 <p>Type 2E10-350</p>	 <p>Type E10-350</p>